

United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandra, Virginia 22313-1450 www.uspio.gov

FILING OR 371 DRAWINGS TOT CLMS IND CLMS ATTY.DOCKET NO FIL FEE REC'D ART UNIT APPL NO. (c) DATE 35 29 4041P-20/DVA 1356 2811 10/601,452 06/23/2003

27572 HARNESS, DICKEY & PIERCE, P.L.C.

P.O. BOX 828 BLOOMFIELD HILLS, MI 48303 MAR 2 9 200

CONFIRMATION NO. 9754

FILING RECEIPT *OC000000010975982*

Date Mailed: 10/03/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Kenji Kohno, Gifu, JAPAN;

Domestic Priority data as claimed by applicant

This application is a DIV of 09/526,971 03/16/2000 PAT 6,614,633

Foreign Applications

JAPAN JPSN 11-210908 07/26/1999 JAPAN JPSN 11-75423 03/19/1999 Should be-75243

If Required, Foreign Filing License Granted: 10/02/2003

Projected Publication Date: 01/08/2004

Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor device including a surge protecting circuit

Preliminary Class

REQUEST FOR CORRECTED OFFICIAL FILING RECEIPT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No.:

10/601,452

Filing Date:

June 23, 2003

Applicant(s)

Kenji Kohno

Group Art:

2811

Title:

Semiconductor Device Including

A Surge Protecting Circuit

Docket No.:

4041P-000020/DVA

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Attn:

Application Processing Division's

Customer Correction Branch

Sir:

We acknowledge receipt of the Official Filing Receipt for the above-identified application, a copy of which is attached. However, the second Foreign Application Priority data is incorrect, and should be as follows:

Foreign Application:

"JAPAN 11-75423" should be -- JAPAN 11-75243 --

A corrected Application Data Sheet has been submitted to the Patent Office prior to this time and Applicant respectfully requests that a corrected filing receipt be issued for the file.

Respectfully submitted,

Dated: March 29, 2004

HARNESS, DICKEY & PIERCE, P.L.C. P.O. Box 828 Bloomfield Hills, MI 48303 (248) 641-1600 H. Keith Miller, Esq. Reg. No. 22,484 Attorney for Applicants

Attachment

EV 406 074 838 US

EV 406 074 83